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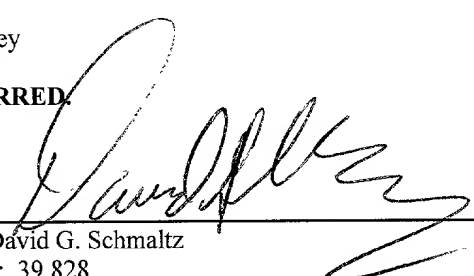
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SPLITTER ARCHITECTURE FOR A TELECOMMUNICATIONS SYSTEM

Background of the Invention

Telecommunications systems commonly include cables containing
5 bundles of twisted pairs of conductors for transmitting telecommunications signals (e.g.,
voice only signals, data only signals, and combined/mixed voice and data signals, etc.).
In these systems, mating connectors (e.g., 25 pair Telco or Amp connectors) are used to
couple the cables to telecommunications equipment for processing. In a twisted pair
telephone carrier system servicing residential and/or businesses, the system may include
10 an MDF (Main Distribution Frame), a POTS (Plain Old Telephone Service) splitter for
separating voice and data signals, and a DSLAM (Digital Subscriber Line Access
Multi-Plexer). For most systems, it is desirable to maximize the splitter densities.
Other important considerations include scalability, serviceability, reduction of cross-
talk, ease of manufacture and cost.

Summary of the Invention

One aspect of the present invention relates to a splitter architecture that
accommodates both scalability and serviceability for voice band and ISDN (Integrated
Services Digital Network) applications of a digital subscriber line (DSL), while fully
supporting lifeline POTS and lifeline ISDN service requirements. By way of example,
20 the DSL's can include asymmetrical digital subscriber lines (ADSL) or very high speed
digital subscriber lines (VDSL).

Another aspect of the present invention relates to a telecommunications
system capable of providing increased splitter densities.

Still another aspect of the present invention relates to a
25 telecommunications system including a splitter chassis having a back plane, and back
plane interface cards mounted at generally perpendicular orientations relative to the
back plane. The perpendicular orientation of the back plane interface cards provides for
improved splitter densities, and also improves scalability by allowing the same type of
back plane interface cards to be used with chassis of different sizes by merely varying
30 the number of interface cards mounted within the chassis.

Still another aspect of the present invention relates to a back plane interface card including card edge connectors for providing connections with a splitter card. The interface card also includes cable connectors for inputting mixed voice and data, and for outputting data signals and voice signals. Tracings are provided on the interface card for interconnecting the cable connectors and the card edge connectors.

5 The tracings are configured such that first tracings carrying data signals from the splitter card to the cable connectors do not cross second tracings carrying voice signals from the splitter card to the cable connectors.

A further aspect of the present invention relates to a telecommunications component including a circuit board, a first multi-pair cable connector, a second multi-pair cable connector, a third multi-pair connector and one or more card edge connectors. The first multi-pair connector is used to input twisted pair, mixed data/voice signal to the circuit board. The second multi-pair cable connector outputs twisted pair, voice signals from the circuit board. The third multi-pair cable connector

15 outputs twisted pair, data signals from the circuit board. Alternatively, if the signals are to be filtered at a downstream location (e.g., at a DSLAM), the third multi-pair connector can output mixed voice and data signals. The card edge connectors include a group of normally closed contacts, and a group of normally open contacts. All of the normally opened contacts are grouped separately from the normally closed contacts.

20 First tracings are provided on the circuit board for connecting the first multi-pair cable connector to the normally closed contacts. Second tracings are provided on the circuit board for connecting the normally closed contacts to the second multi-pair cable connector. Third tracings are provided on the circuit board for connecting the normally open contacts to the third multi-pair cable connectors. The traces are positioned such

25 that none of the third tracings on the circuit board cross over any of the first or second tracings.

An additional aspect of the present invention relates to a chassis defining a back plane, and a back plane interface card mounted at the back plane of the chassis. The interface card includes a circuit board positioned at an orientation generally perpendicular with respect to the back plane. The interface card also includes a first

30 multi-pair cable connector, a second multi-pair cable connector, a third multi-pair cable connector and one or more card edge connectors. The first multi-pair cable

Figure 6 is a schematic depiction of an inventive configuration for a back plane interface card and a splitter card, both the back plane interface card and the splitter card are aligned at a generally perpendicular orientation relative to a chassis back plane;

Figure 7 is a perspective view of a splitter chassis constructed in accordance with the principles of the present invention, an endplate of the chassis has been removed and a splitter card has been slid from the chassis;

Figure 8 is a rear perspective view of the splitter chassis of Figure 7 with the back plane removed;

Figure 9 is a rear view of the loaded splitter chassis of Figure 7;

Figure 10 is a front view of the loaded splitter chassis of Figure 7;

Figure 11 is an exploded view of the splitter chassis of Figure 7 with the splitter cards and the back plane interface cards removed;

Figure 12 is a front perspective view of another chassis constructed in accordance with the principles of the present invention;

Figure 13 is a rear perspective view of the splitter chassis of Figure 12; and

Figure 14 is a front view of the splitter chassis of Figure 12.

While the invention is amenable to various modifications and alternative forms, specifics thereof have been shown by way of example in the drawings and will be described in detail below. It is to be understood, however, that the intention is not to limit the invention to the particular embodiments described. On the contrary, the invention is intended to cover all modifications, equivalents, and alternatives falling within the scope of the invention as defined by the appended claims.

Detailed Description

In the following detailed description, references are made to the accompanying drawings that depict various embodiments in which the invention may be practiced. It is to be understood that other embodiments may be utilized, and structural and functional changes may be made without departing from the scope of the present invention.

Referring now to Figure 1, a telecommunications system 10 is shown including an arrangement of equipment. The system 10 is representative of a telephone carrier's system for transmitting voice and data to residences and businesses. A main distribution frame (MDF) 12 is linked to a splitter device 16 (e.g., a card including POTS splitter circuits or ISDN splitter circuits). The MDF 12 is also linked to one or more DSLAM modules 18, and a voice switch 19 (e.g., a switch equipped with POTS interface line cards or ISDN interface line cards).

In use of the system 10, the splitter device 16 receives a mixed voice and data signal from the MDF 12. The splitter device 16 splits the mixed signal into split signals, and then filters the split signals. For example, one of the split signals can be filtered to provide a voice only signal (i.e., the high frequency data portion of the signal is filtered out), while the other split signal can be filtered to provide a data only signal (i.e., the low frequency voice portion of the signal is filtered out). The data only signals are passed from the splitter device 16 to the DSLAM 18. The voice only signals are passed from the splitter device 16 to the MDF 12 for transmission to the voice switch 19.

Figure 2 shows a similar telecommunications system 10' having the same components as those described with respect to the telecommunications system 10 of Figure 1. However, in the embodiment of Figure 2, voice signals are transmitted directly from the splitter device 16 to the voice switch 19.

Referring still to Figs. 1 and 2, the MDF 12, the POTS splitter device 16, the DSLAM 18 and the voice switch 19 are typically interconnected by cables 21. The cables 21 preferably each include multiple pairs of conductors for transmitting separate twisted pair signals. By way of example, the cables 21 can comprise 25 pair cables (i.e., cables each containing 25 pairs of wire conductors for transmitting 25 separate twisted pair signals). Multi-pair connectors are used to provide interconnections between the cables 21 and the components of the telecommunications system 10. For example, multi-pair connectors are typically provided at the ends of the cables 21. The multi-pair connectors at the ends of the cables 21 are commonly coupled to corresponding multi-pair cable connectors mounted at the equipment to provide connections thereinbetween.

An exemplary multi-pair cable connector 20 is shown in Figures 3A and 3B. The depicted connector 20 is a conventional connector such as a Telco or Amp connector. Preferably, the connector 20 is adapted for use with a 25 pair cable. Thus, the conductor 20 preferably includes 25 pairs of conductors 28. For clarity, only two of the pairs of conductors 28 are shown in Figures 3A and 3B.

The connector 20 includes a first end 26 defining a receptacle 27 sized for receiving a portion of a mating connector (not shown) connected at the end of a cable. The pairs of conductors 28 are positioned within the receptacle 27, and are adapted to contact corresponding conductor pairs of the mating connector. The conductors 28 extend through the connector 20 from the first end 26 to a second end 30. Portions of the conductors 28 located at the second end 30 are used to provide connections to conductor pairs of cables, wires, circuit boards, equipment, etc.

The splitter 16 of Figs. 1 and 2 can be arranged in any number of known configurations. One known splitter configuration includes a chassis having a back plane. An interface card is mounted at the back plane. The interface card includes circuit board that is parallel to the back plane of the chassis. An array of cable connectors mounted on the circuit board. The array of cable connectors includes cable connectors for inputting mixed data/voice signals to the circuit board, cable connectors for outputting voice signals, and cable connectors for outputting data signals from the circuit board. Multiple sets of card edge connectors are provided on the circuit board for allowing multiple splitter cards to interface with the circuit board. The card edge connectors include a number of normal-through connections for providing full support of lifeline POTS and lifeline ISDN services. A plurality of tracings interconnect the array of cable connectors to the sets of card edge connectors. The tracings are arranged such that first tracings carrying data signals from the splitter cards cross over second tracings carrying voice signals from the splitter cards.

The above described configuration is problematic for a number of reasons. For example, the described crossing over of tracings may increase the cross talk that occurs at the back plane. Also, the described crossing over of tracings requires the circuit board to have a relatively large number of layers thereby increasing the manufacturing costs associated with the circuit board. Further, the configuration of the

back plane interface card is not readily scalable because the size of the back plane circuit board will often need to be varied to be used with chassis of different sizes.

Figures 4A and 4B schematically illustrate a splitter architecture 40 in accordance with the principles of the present invention. The architecture 40 is particularly useful with telecommunication service options such as asymmetrical digital subscriber lines (ADSL) and very high speed digital subscriber lines (VDSL). While the signals in the schematics are shown traveling in one direction, it will be appreciated that the system is preferably bi-directional.

The architecture 40 includes a back plane interface card 42 adapted to couple with a splitter card 44. The interface card 42 is preferably mounted at a back plane 46 of a splitter chassis or housing, and includes a circuit board 48 adapted to align at a generally perpendicular angle relative to the back plane 46. The circuit board 48 includes a back edge 50 positioned generally at the back plane 46, and a front edge 52 that is forwardly offset from the back plane 46. First, second and third multi-pair cable connectors 54, 56, and 58 are mounted at the back edge 50, and first and second card edge connectors 60 and 62 are mounted at the front edge 52. The first cable connector 54 is adapted for inputting twisted pair, mixed data/voice signals to the interface card 42. The second and third cable connectors 56 are used to respectively output twisted pair, voice signals and twisted pair data signals from the interface card 42. The connectors 54-58 can have the same configuration as the connector shown in Figures 3A and 3B. Referring to Figure 5D, a few representative conductors 28 are shown within the connectors 54-56.

The first and second card edge connectors 60 and 62 are used to provide a connection between the interface card 42 and the splitter card 44. For example, the splitter card 44 includes first and second card edge portions 64 and 66 that are respectively received within the first and second card edge connectors 60 and 62. The card edge portion 64 includes a pair of contacts 68b (e.g., contact pads) on one side of the splitter card 44, and another pair of contacts 70b (e.g., contact pads) on the other side of the splitter card 44. When the first card edge portion 64 is inserted within the first card edge connector 60, the contacts 68b and 70b engage respective contacts 68a and 70a of the first card edge connector 60. The contacts 68a and 70a, best shown in Figure 5C, are preferably spring contacts that normally engage one another (i.e., the

contacts are normally closed). Figure 5C schematically shows a few representative contacts 68a and 70a. When the first card edge portion 64 is inserted within the first card edge connector 60, the contacts 68a and 70a are forced apart and placed into engagement with the contacts 68b and 70b of the splitter card to provide a connection thereinbetween.

Referring again to Figures 4A and 4B, the second card edge portion 66 of the splitter card 44 includes contacts 72 (e.g., contact pads) for providing a connection with the second card edge connector 62. For example, when the second card edge portion 66 is inserted into the second card edge connector 62, the contacts 72 engage respective contacts 74 provided at the second card edge connector 62 to provide a connection thereinbetween. The contacts 74 are preferably normally open (i.e., no signals are passed through the contacts 74 unless the splitter card 44 is coupled to the interface card 42). Figure 5C schematically shows a few representative few contacts 74. As shown in Figure 5C, the contacts 74 include two groups separated by a gap sized for receiving the card edge portion 66. Thus, the groups of contacts 74 are adapted for engaging contacts 72 located on opposite sides of the splitter card.

For clarity purposes, only one twisted pair circuit for the splitter architecture is shown in Figures 4A and 4B. Thus, only single pairs of the contacts 68a, 68b, 70a, 70b, 72 and 74 are shown. However, it will be appreciated that the actual number of contacts provided will be dependent upon the capacity of the connectors 54, 56, and 58; and the number of splitters provided at the splitter card 44. For example, if the cable connectors 54–56 and 58 comprise conventional 25 pair connectors, 24 separate pairs of the contacts 68a, 68b, 70a, 70b, 72 and 74 are preferably provided. In such a case, the twenty fifth pair of conductors in the cable connectors is preferably grounded or inactive. Thus, in such an embodiment, the splitter architecture 40 has the capacity to process 24 separate twisted pair signals. Of course, the capacity of the splitter architecture 40 can be varied. For example, in certain embodiments it may be desirable to provide more or fewer splitters at the splitter card 44 (e.g., any number of splitters can be provided such as 8, 24, 48, 96, etc.). Thus, the number of separate circuit paths provided by the splitter architecture 40 can be varied accordingly.

With respect to the splitter card 44, it is preferred for all of the contacts 68b and 70b to be grouped separately from the contacts 72. For example, all of the

contacts 68b and 70b of the splitter card are preferably provided on the first card edge portion 64, while all of the contacts 72 of the splitter card are preferably provided at the second card edge portion 66. In a card with 24 splitters, 48 of the contacts 72 can be provided at the second card edge portion 66, and 48 of each of the contacts 68b and 70b can be provided at the first card edge portion 64.

Referring still to Figures 4A and 4B, the cable connector 54 is connected to the pair of contacts 68a by first tracings 76; the pair of contacts 70a is connected to cable connector 56 by second tracings 78; and the pair of contacts 74 is connected to cable connector 58 by third tracings 80. Once again, for clarity and ease of explanation, the circuit pathway for only a single twisted pair signal has been illustrated in Figures 4A and 4B.

Figures 5A–5D show the back plane interface card 42 equipped with 24 separate circuits capable of handling 24 different twisted pair signals. In the embodiment of Figures 5A–5D, all of the normally closed contacts 68a and 70a are provided at the first card edge connector 60, and all of the normally open contacts 74 are provided at card edge connector 62. Thus, the normally closed contacts 68a and 70a are grouped separately from the normally open contacts 74. This particular configuration is advantageous because a more simplified tracing layout can be used. For example, because the normally closed contacts 68a and 70a are grouped separately from the normally open contacts 74, none of the third tracings 80 are not required to cross over any of the first or second tracings 76 and 78. This helps reduce manufacturing costs by reducing the number of layers required to manufacture the circuit board 48. Additionally, the reduction in crossing of the tracings may assist in reducing cross talk at the back plane 46.

Figure 5E shows a multi-layer view of another interface card 42' constructed in accordance with the principles of the present invention. The card has a similar construction as the card 42 shown in Figures 5A–5D. For example, both cards include cable connectors 54–56, as well as card edge connectors 60 and 62. Also, card 42' has tracings 76', 78' and 80' that serve the same functions as tracings 76, 78 and 80 of the card 42; but are arranged in a slightly different pattern. As previously described with respect to tracings 76–80, tracings 76', 78' and 80' are preferably configured such that none of tracings 80' cross over any of tracings 76' and 78'.

As shown in Figures 5A–5E, the normally open and closed contacts are grouped separately by placing such contacts on separate connectors. It will be appreciated that alternative grouping techniques can also be used. For example, the normally open and closed contacts can be separately grouped (i.e., not mixed or alternated) on a common connector. In one embodiment, all of the contacts can be grouped separately on a single connector. Additionally, for some applications, it may be desirable to use more than two connectors. For example, the group of normally closed connectors could be split between two connectors and the open contacts could be provided on a third connector.

Referring back to Figures 4A and 4B, the splitter card 44 includes a plurality of splitters 82 (e.g., 24 splitters). The contacts 68b, 70b and 72 are shown connected to one of the splitters 82 by tracings. For example, tracings 84 connect the contacts 68b to the splitter 82, tracings 86 connect the contacts 70b to the splitter 82, and tracings 88 connect the contacts 72 to the splitter 82. While not shown for clarity purposes, it will be appreciated that similar contacts and tracings are provided for each of the twenty four splitters 82 shown in Figures 4A and 4B.

The splitters 82 can have a number of different configurations. For example, the splitters 82 can comprise POTS splitter circuits. A conventional POTS circuit functions to split a composite signal (i.e., a mixed voice/data signal) into two composite signals. One of the split composite signals is typically passed through one or more low pass filters capable of passing the relatively lower frequency voice content of the composite signal (e.g., less than about 4 kHz) and rejecting the composite signal content above the voice band (e.g., 30 kHz and above). The other split composite signal can be passed through a high pass filter that passes the composite signal content associated with the data band (e.g., about 30 kHz and above), and rejects the relatively lower frequency voice content of the composite signal. Alternatively, the other split signal can be unfiltered such that the signal remains a composite signal. For such an embodiment, it is assumed that the DSLAM or other digital multi-plexer that ultimately receives the composite signal will provide any required high-pass filter elements to remove the relatively low frequency voice signal content of the composite signal. It will further be appreciated that ISDN filter circuits could also be used.

Figures 4A and 4B show two modes of operation for the back plane interface card. Figure 4A shows the back plane interface card 42 in a normally closed mode in which the interface card 42 is not connected to the splitter card 44. In such a mode, an analog voice signal (e.g., from an MDF) is inputted to the interface card 42 through connector 54. Once at the interface card 42, the voice signal is transmitted through the first tracings 76 to contact 68a. Because the splitter card 44 is not connected to the interface card 42, the contacts 68a and 70a are closed. Thus, the voice signal is transmitted through the contacts 68a and 70a to the second tracings 78. From the second tracings 78, the voice signal is transmitted through connector 56 and is outputted to another piece of equipment (e.g., an MDF or switch). In this manner, the normally closed contacts 68a and 70a at the card edge connector 60 fully support lifeline POTS and lifeline ISDN services.

Figure 4B shows the splitter card 44 connected to interface card 42. When the splitter card 44 is connected to the interface card 42, the normally closed contacts 68a, 70a are opened and placed into respective engagement with contacts 68b and 70b of the splitter card 44. Also, contacts 74 of the second card edge connector 62 are concurrently closed (i.e., the contacts 74 are placed into engagement with contacts 72 of the splitter card 44). Thus, a composite signal inputted through connector 54 will be transmitted from cable connector 54 to the splitter 82 via tracings 76 and 84. At the splitter 82, the composite signal is preferably split and filtered such that a voice only signal is transmitted through tracings 86, and a data only signal is transmitted through tracings 88. The voice only signal is transmitted from tracings 86 through contacts 70b and 70a to tracings 78. From tracings 78, the voice only signal is outputted from cable connector 56 to a piece of equipment (e.g., an MDF or switch). The data only signal is transmitted through contacts 72 and 74 to tracings 80. From tracings 80, the data only signal is output through connector 58 to a piece of equipment (e.g., a DSLAM).

Figure 6 is a schematic illustration showing the relative orientations between the back plane interface card 42 and the splitter card 44. As shown, when the splitter card 44 is connected to the interface card 42, the two cards are aligned generally parallel with respect to one another. Thus, as so positioned, both of the cards 42 and 44 are aligned perpendicular relative to the back plane 46 of the chassis. Such a configuration has been found to assist in greatly increasing the splitter density of a given

chassis. Such a configuration also improves scalability by allowing the signal sized unfaced cards 42 to be used with chassis of different sizes. Further flexibility can be provided by either mounting the back plane interface cards 42 in a side by side relationship within a chassis, or by stacking the interface cards 42 one on top of the other within a chassis.

Figure 7 illustrates a splitter chassis 90 constructed in accordance with the principles of the present invention. The chassis 90 is generally rectangular and includes a top side 92 positioned opposite from a bottom side 94. End plates 96 are provided for enclosing opposite ends of the chassis 90. A back plane plate 98 (as shown in Figure 8) defines a back plane of the chassis 90, and a front side 100 of the chassis 90 is generally open. The chassis 90 also includes mounting brackets 91 (shown in Figure 8) for connecting the chassis 90 to a structure such as a rack.

As best shown in Figure 8, a plurality of the back plane interface cards 42 are preferably secured to the back plane plate 98 by conventional techniques (e.g., fasteners such as bolts or screws). The back plane plate 98 defines multiple columns of vertically spaced apart openings 102 sized for receiving the cable connectors 54, 56 and 58 of the interface cards 42. When the interface cards 42 are connected to the back plane plate 98, the connectors define an array of connectors as shown in Figure 9.

Referring to Figure 11, the chassis 90 includes intermediate dividers 104 that provide additional structural rigidity to the chassis 90. The chassis 90 also includes top and bottom tracks 106 and 108 respectively secured to the top side 92 and the bottom side 94 of the chassis 90. The top and the bottom tracks 106 and 108 oppose one another and define elongated slots that extend between the front and back sides of the chassis 90. As shown in Figure 7, the splitter cards 44 are mounted within the chassis 90 by sliding the splitter cards 44 through the open front side 100 into the tracks 106 and 108. Preferably, top and bottom ends of the splitter cards 44 are respectively received within the upper and lower tracks 106 and 108. In this manner, the tracks function to hold the splitter cards in a parallel, slightly spaced apart relation relative to one another. Front latches 110 on the splitter cards 44 hook into a top slot 112 of the chassis to retain the splitter cards 44 within the chassis 90.

As best shown in Figures 7 and 10, the back plane interface cards 42 are also mounted within the tracks 106 and 108. For example, as shown in Figure 10, top

and bottom edges of the interface cards 42 are received within the tracks 106 and 108. In this manner, the tracks 106 and 108 assist in holding the back plane interface cards 42 in a perpendicular relationship relative to the back plane plate 98. The top and bottom tracks 106 and 108 also assist in maintaining a generally uniform spacing between the interface cards 42. Preferably, each back plane interface card 42 is mounted in the same set of tracks as its corresponding splitter card 44. With such a configuration, the tracks 106 and 108 assist in providing registry between the interface cards 42 and the splitter cards 44.

Figures 12-14 illustrate an alternative chassis 90' constructed in accordance with the principles of the present invention. The chassis 90' has a rectangular shape and is sized for receiving a single splitter card 44 and a single back plane interface 42. Similar to the chassis of Figure 7, the chassis 90' includes opposing tracks 106' and 108' for receiving the edges of the splitter card 44 and the interface card 42.

The above specification, examples and data provide a complete description of the manufacture and use of the composition of the invention. Since many embodiments of the invention can be made without departing from the spirit and scope of the invention, the invention resides in the claims hereinafter appended.

WE CLAIM:

1. A telecommunications component comprising:
 - a circuit board;
 - a first multi-pair cable connector for inputting twisted pair, mixed data/voice signals to the circuit board;
 - a second multi-pair cable connector for outputting twisted pair, voice signals from the circuit board;
 - a third multi-pair cable connector for outputting twisted pair, data signals or mixed data/voice signals from the circuit board;
 - one or more card edge connectors connected to the circuit board, the one or more card edge connectors including:
 - a group of normally closed contacts;
 - a group of normally open contacts, all of the normally open contacts being grouped separately from the normally closed contacts;
 - first tracings provided on the circuit board for connecting the first multi-pair cable connector to the normally closed contacts;
 - second tracings provided on the circuit board for connecting the normally closed contacts to the second multi-pair cable connector;
 - third tracings provided on the circuit board for connecting the normally open contacts to the third multi-pair cable connector; and
 - the tracings being positioned such that none of the third tracings on the circuit board cross-over any of the first or second tracings.
2. The telecommunications component of claim 1, further comprising a splitter card adapted for connection to the one or more card edge connectors of the circuit board, the splitter card including a plurality of splitters for splitting the mixed data/voice signals into the data signals and the voice signals.
3. The telecommunications component of claim 2, wherein the splitters comprise POTS splitters.

4. The telecommunications component of claim 3, wherein the splitter card includes at least 24 of the POTS splitters.
5. The telecommunications component of claim 1, wherein the one or more card edge connectors include a first card edge connector and a separate second card edge connector, the first card connector including the normally closed contacts and the second card edge connector including the normally open contacts.
6. The telecommunications component of claim 5, further comprising a splitter card adapted for connection to the first and second card edge connectors of the circuit board, the splitter card including a plurality of splitters for splitting the mixed data/voice signals into the data signals and the voice signals.
7. The telecommunications component of claim 6, wherein the splitters comprise POTS splitters.
8. The telecommunications component of claim 7, wherein the splitter card includes at least 24 of the POTS splitters.
9. The telecommunications component of claim 1, further comprising a chassis including a reference back plane at which the first, second and third multi-pair cable connectors are positioned, the circuit board being aligned generally at a perpendicular orientation relative to the reference back plane.
10. The telecommunications component of claim 9, further comprising a splitter card adapted for connection to the one or more card edge connectors of the circuit board, the splitter card including a plurality of splitters for splitting the mixed data/voice signals into the data signals and the voice signals, the splitter card being aligned at a generally parallel orientation relative to the circuit board when the splitter card is connected to the one or more card edge connectors.
11. The telecommunications component of claim 10, wherein the splitters comprise POTS splitters.

12. The telecommunications component of claim 11, wherein the splitter card includes at least 24 of the POTS splitters.

13. The telecommunications component of claim 9, wherein the chassis includes opposing slots, wherein the circuit board is mounted in the slots.

14. The telecommunications component of claim 13, further comprising a splitter card adapted for connection to the one or more card edge connectors of the circuit board, the splitter card including a plurality of splitters for splitting the mixed data/voice signals into the data signals and the voice signals, the splitter card and the circuit board being mounted in the same opposing slots when the splitter card is connected to the circuit board by the first and second card edge connectors.

15. A telecommunications component comprising:
a chassis defining a reference back plane;
an interface card mounted at the reference back plane of the chassis, the interface card including:
a circuit board positioned at an orientation generally perpendicular with respect to the back plane;
a first multi-pair cable connector positioned at the back plane for inputting twisted pair, mixed data/voice signals to the circuit board;
a second multi-pair cable connector positioned at the back plane for outputting twisted pair, voice signals from the circuit board;
a third multi-pair cable connector positioned at the back plane for outputting twisted pair, data signals or mixed data/voice signals from the circuit board;
one or more card edge connectors connected to the circuit board, the one or more card edge connectors including normally closed contacts and normally open contacts;
first tracings provided on the circuit board for connecting the first multi-pair cable connector to the normally closed contacts;
second tracings provided on the circuit board for connecting the normally closed contacts to the second multi-pair cable connector; and

third tracings provided on the circuit board for connecting the normally open contacts to the third multi-pair cable connector.

16. The telecommunications component of claim 15, further comprising a splitter card adapted for connection to the one or more card edge connectors of the circuit board, the splitter card including a plurality of splitters for splitting the mixed data/voice signals into the data signals and the voice signals, the splitter card being aligned at a generally parallel orientation relative to the circuit board when the splitter card is connected to the one or more card edge connectors.

17. The telecommunications component of claim 16, wherein the splitters comprise POTS splitters.

18. The telecommunications component of claim 17, wherein the splitter card includes at least 24 of the POTS splitters.

19. The telecommunications component of claim 15, wherein the chassis includes opposing slots, wherein the circuit board is mounted in the slots.

20. The telecommunications component of claim 19, further comprising a splitter card adapted for connection to the one or more card edge connectors of the circuit board, the splitter card including a plurality of splitters for splitting the mixed data/voice signals into the data signals and the voice signals, the splitter card and the circuit board being mounted in the same opposing slots when the circuit board and the splitter card are interconnected by the card edge connectors.

21. The telecommunications component of claim 15, wherein a plurality of the interface cards are mounted within the chassis.

22. The telecommunications component of claim 15, wherein the chassis is sized to hold a single one of the interface card.

23. The telecommunications component of claim 15, wherein all of the normally closed contacts are provided on a first card edge connector, and all of the normally open contacts are provided on a separate second card edge connector.

24. The telecommunications component of claim 23, further comprising a splitter card adapted for connection to the first and second card edge connectors, the splitter card including a plurality of splitters for splitting the mixed data/voice signals into the data signals and the voice signals, the splitter card being aligned at a generally parallel orientation relative to the circuit board when the splitter card is connected to the first and second card edge connectors.

25. The telecommunications component of claim 24, wherein the splitters comprise POTS splitters.

26. The telecommunications component of claim 25, wherein the splitter card includes at least 24 of the POTS splitters.

27. The telecommunications component of claim 23, further comprising a splitter card adapted for connection to the first and second card edge connectors, the splitter card including a plurality of splitters for splitting the mixed data/voice signals into the data signals and the voice signals, the splitter card and the circuit board being mounted in a common pair of opposing slots defined by the chassis. connectors.

28. A splitter card comprising:
a circuit board;
a plurality of splitters mounted on the circuit board, each splitter being adapted for receiving a mixed voice and data signal and outputting a first signal that is voice only and a second signal that is either data only or mixed voice and data;
first tracings for transmitting the mixed voice and data signals from first contacts to the splitters;
second tracings for transmitting the first signals from the splitters to second contacts;

third tracings for transmitting the second signals from the splitters to third contacts; and

the first and second contacts being grouped separately from the third contacts.

29. The splitter card of claim 28, wherein all of the first and second contacts are provided on a first card edge extension that projects out from the circuit board, and all of the third contacts are provided on a second card edge extension that projects outward from the circuit board.

30. The splitter card of claim 28, wherein 24 of the splitters are provided on the circuit board.

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Abstract

An interface card adapted to be mounted at the back plane of a splitter chassis. The interface card includes card edge connectors for providing connections with a splitter card. The interface card also includes cable connectors for inputting
5 mixed voice and data, and for outputting data and voice signals. Tracings are provided on the interface card for interconnecting the cable connectors and the card edge connectors. The tracings are configured such that first tracings carrying data signals from the splitter card to the cable connectors do not cross second tracings carrying voice signals from the splitter card to the cable connectors.

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I hereby certify that this paper or fee is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to BOX PATENT APPLICATION, Assistant Commissioner for Patents, Washington, D.C. 20231.	
By:	DAVID G. SCHMALTZ
Name:	David G. Schmalz

Typical Applications

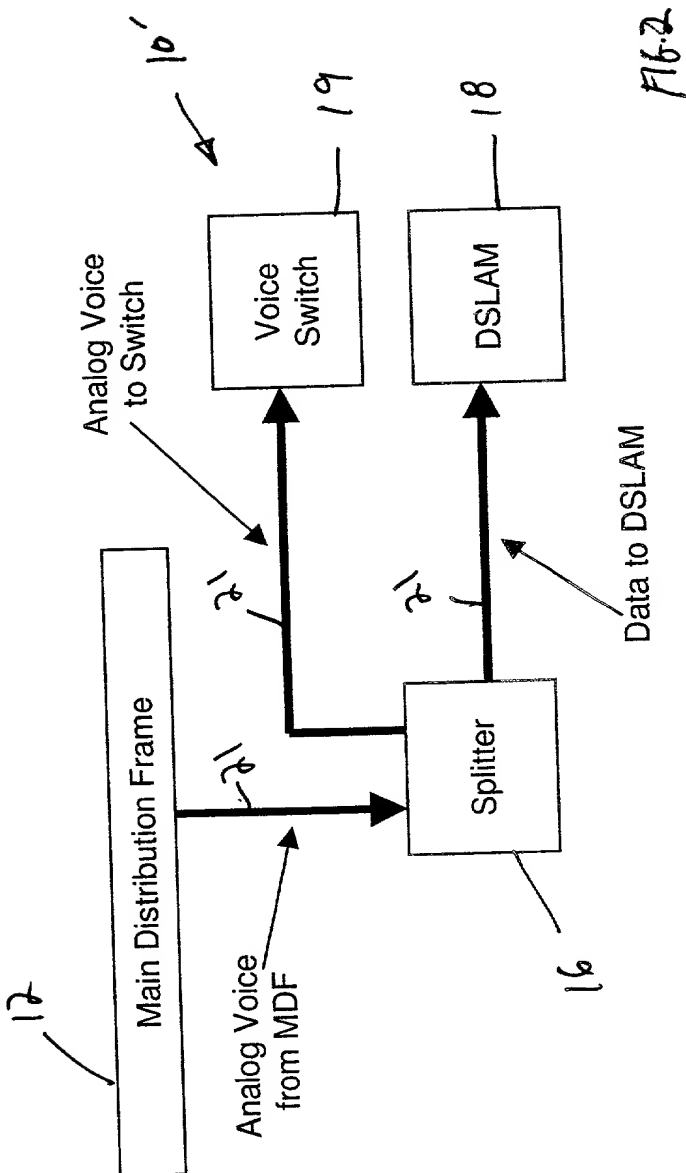
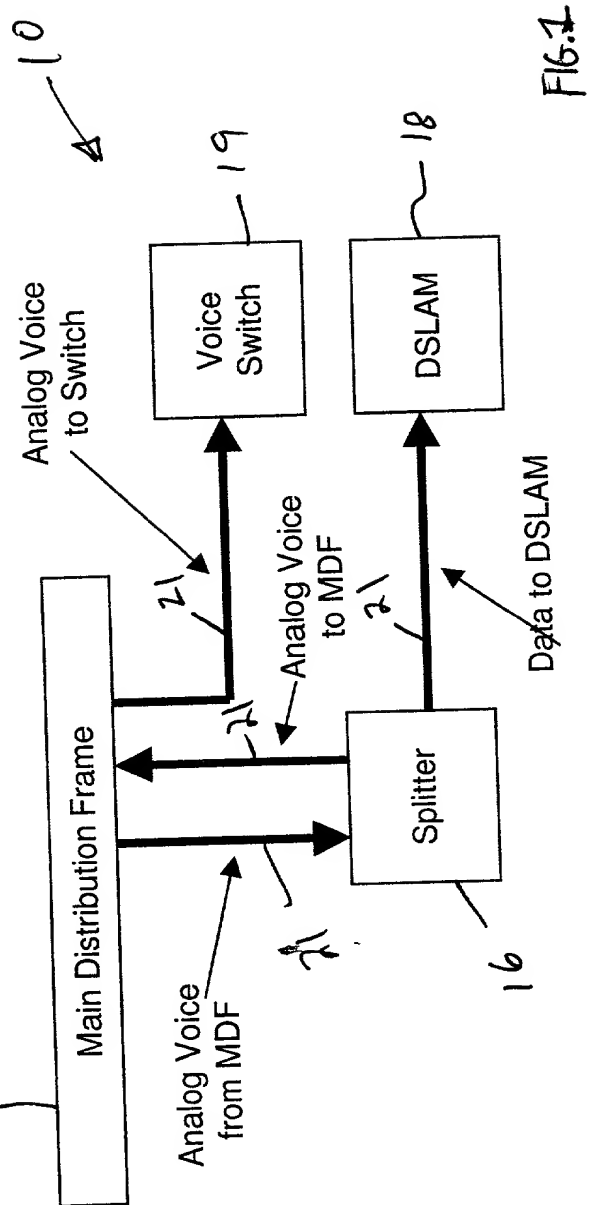


FIG. 3A

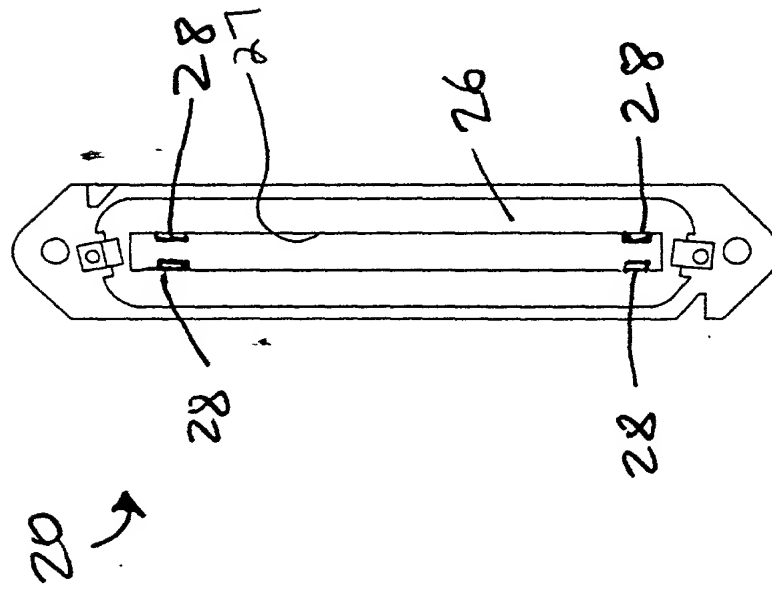
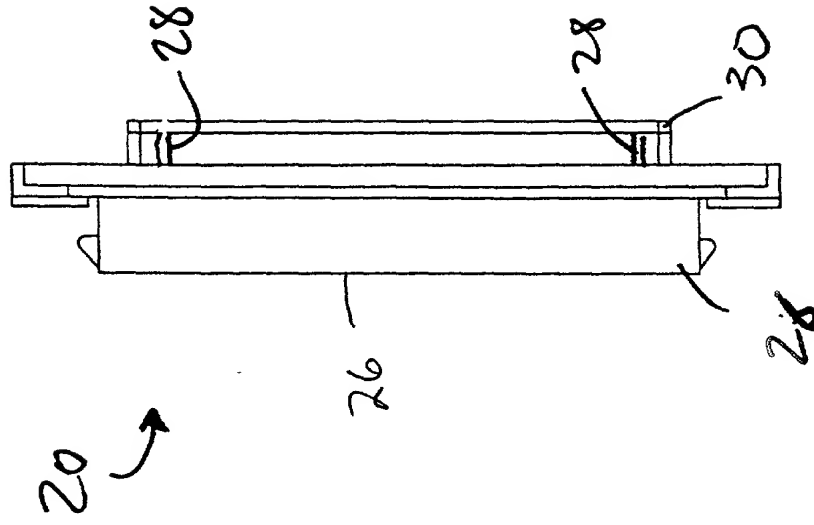


FIG. 3B



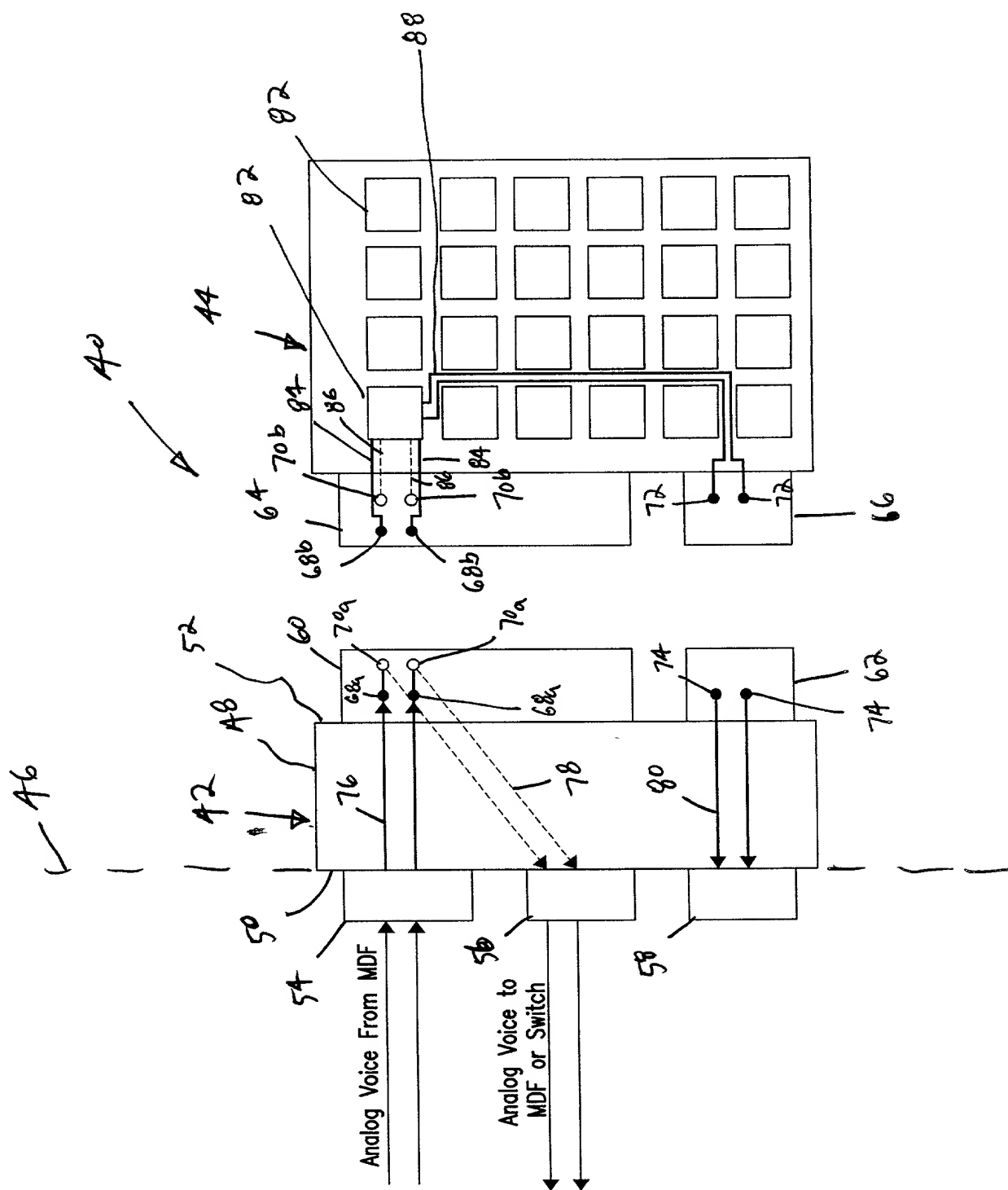
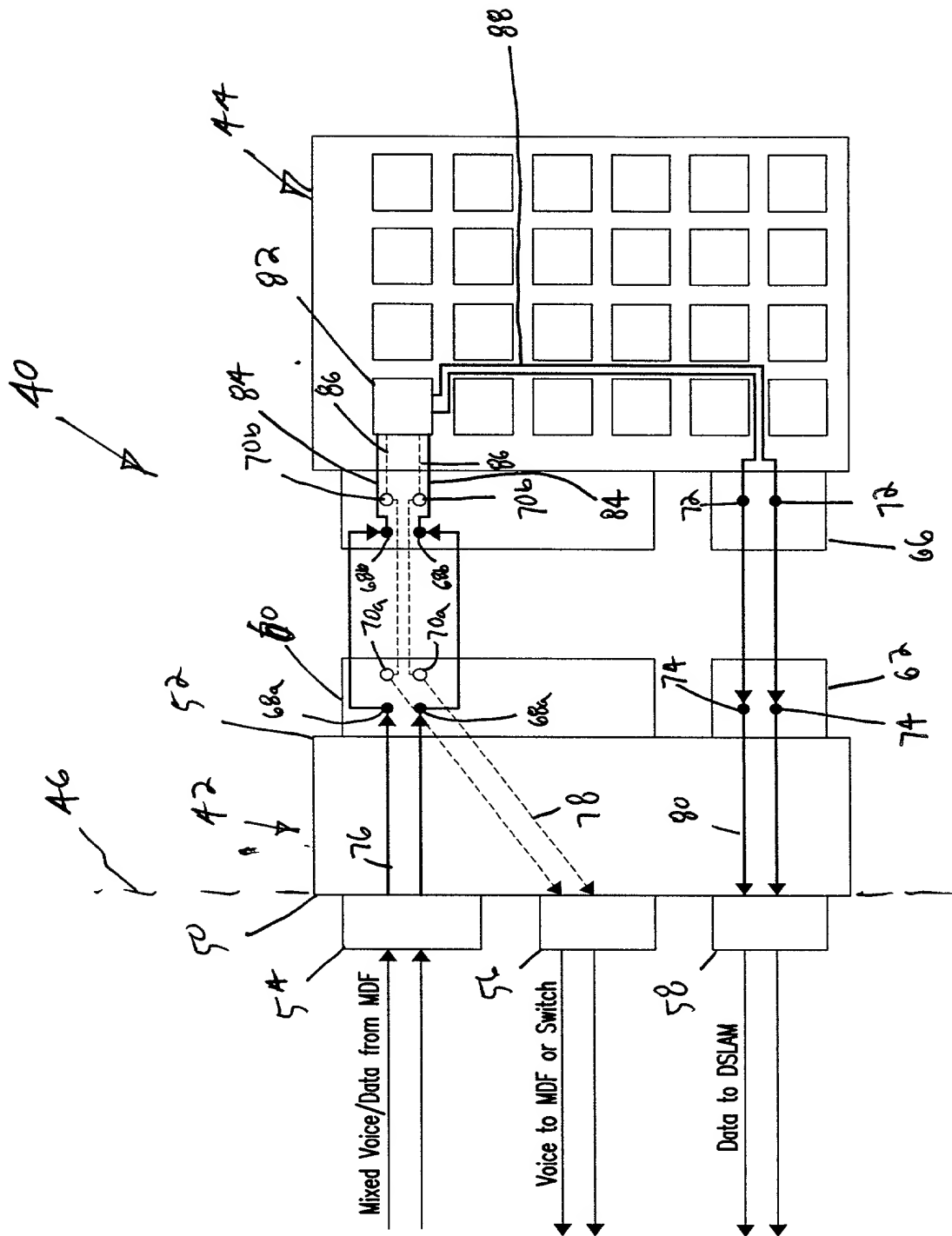
[illegible]

FIG. 4B



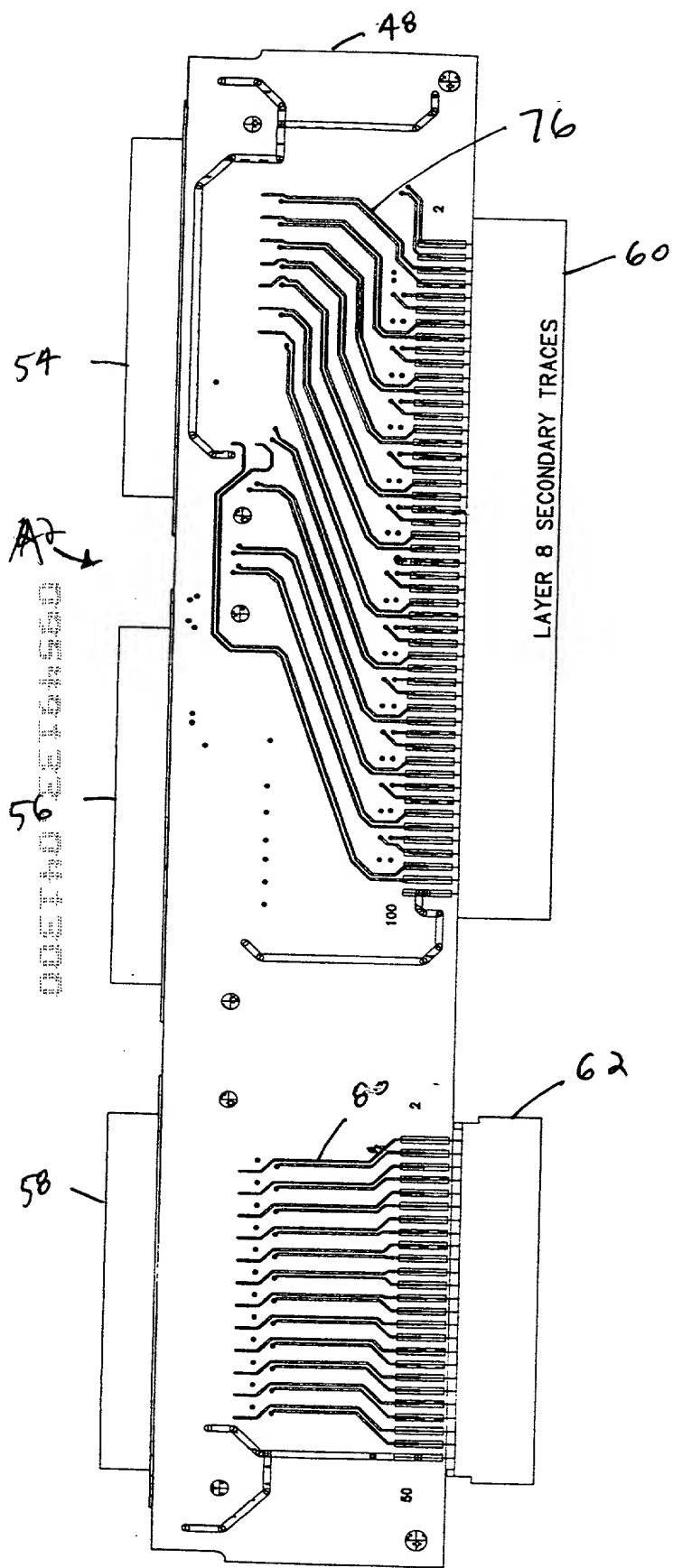


FIG. 5A

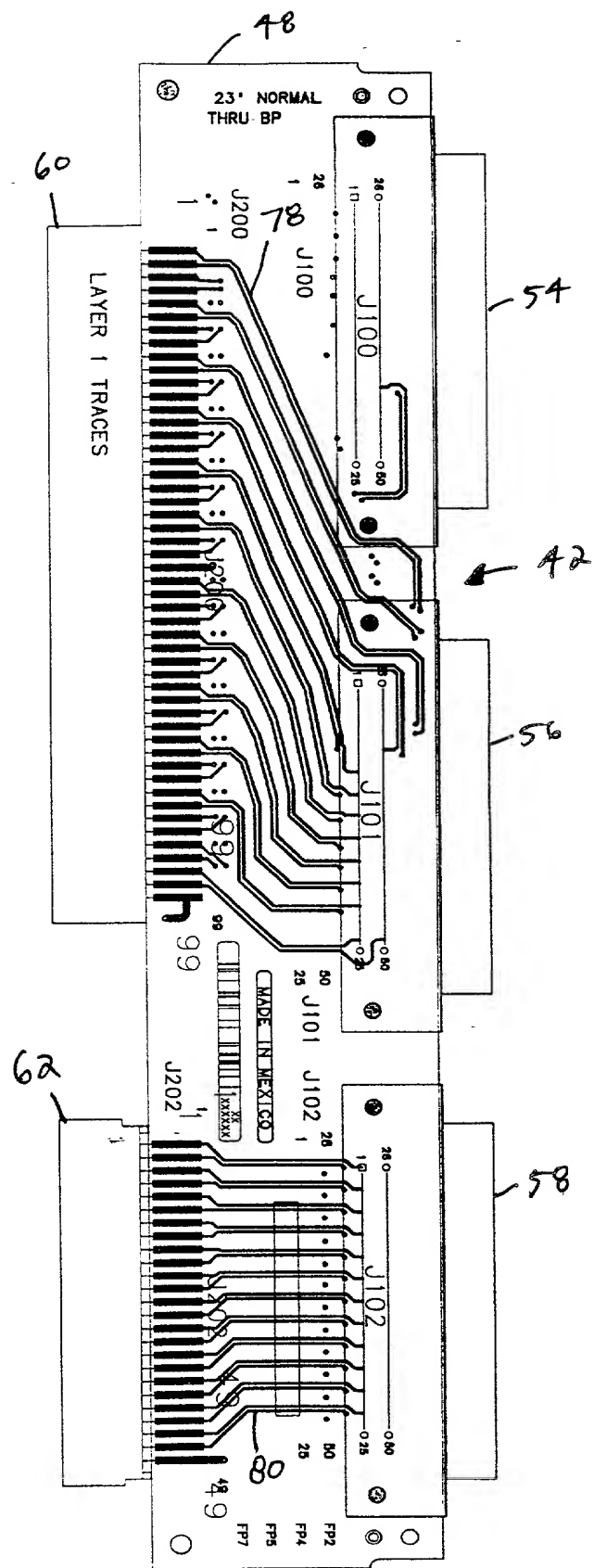


FIG. 5B

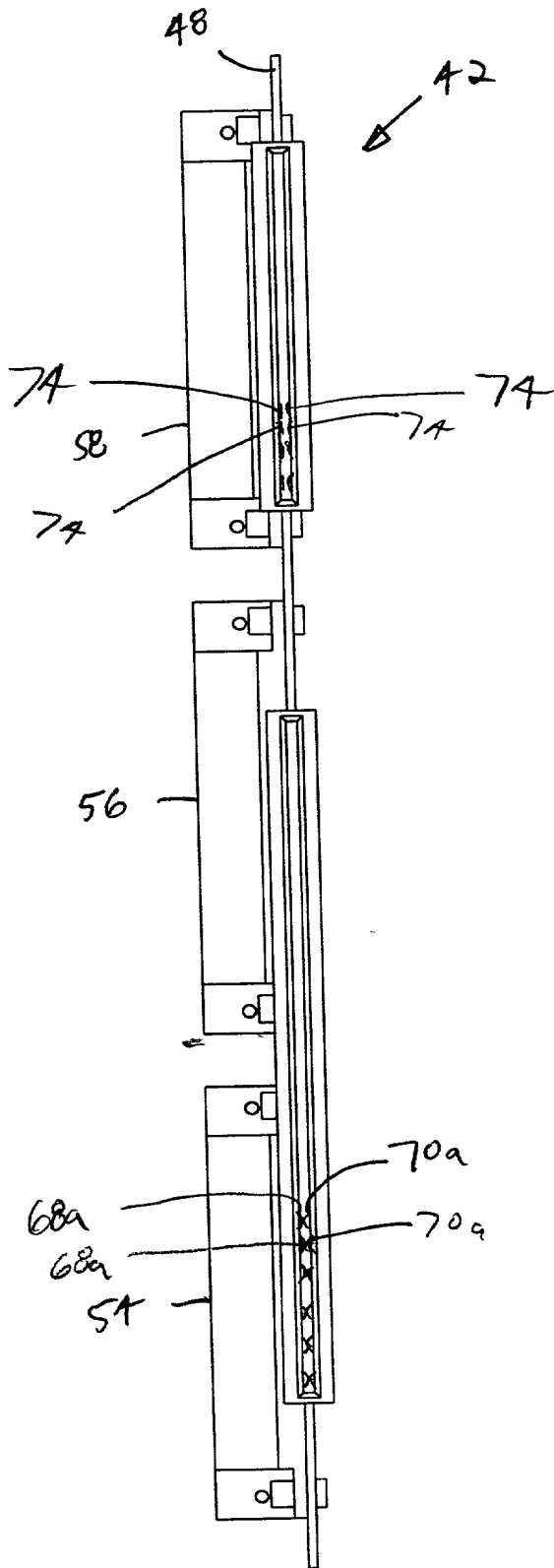


FIG. 5C

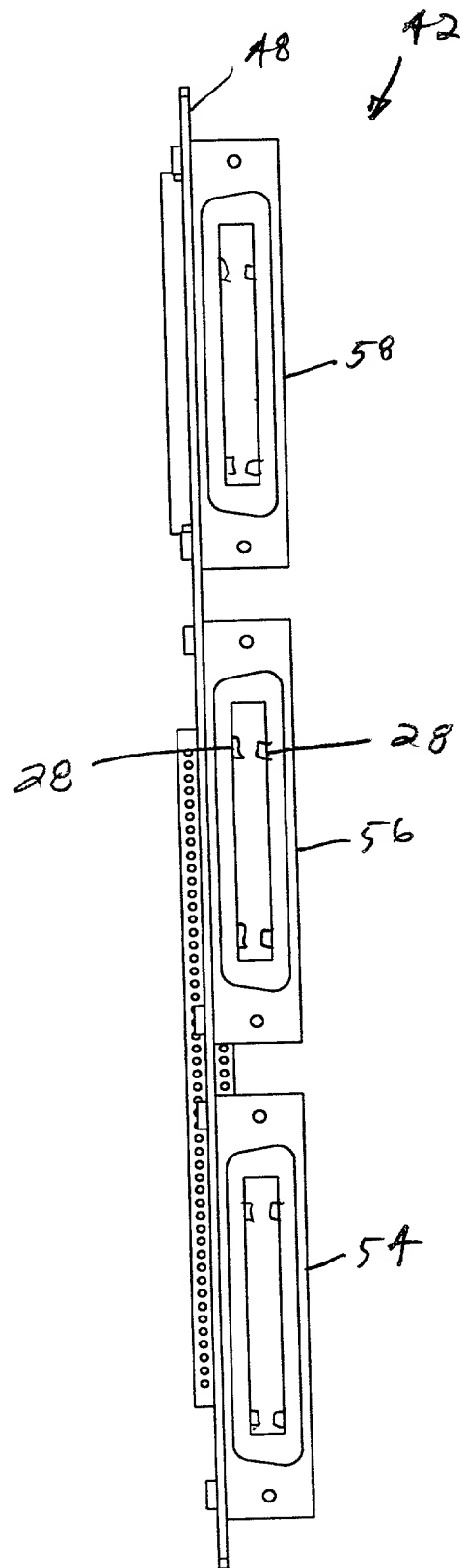
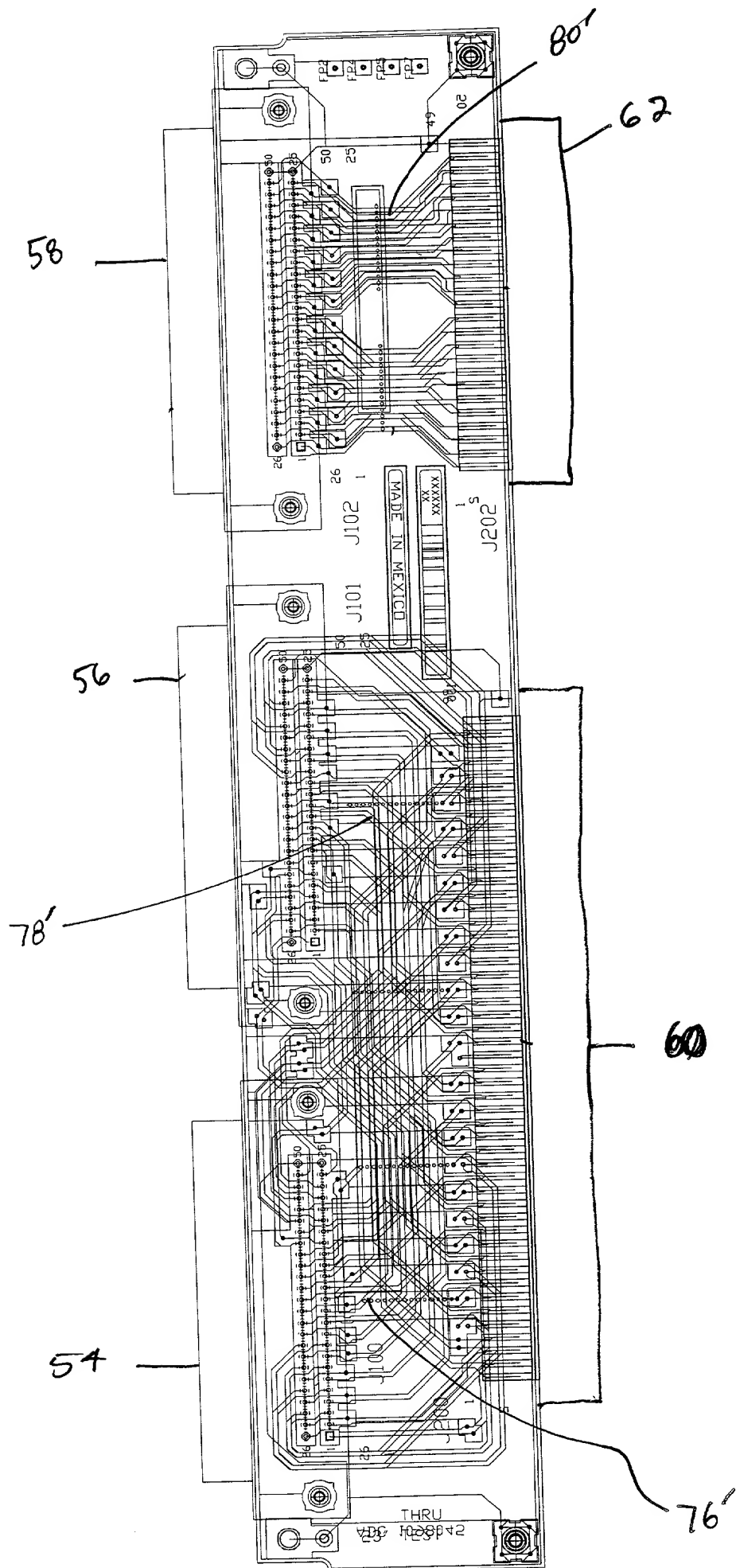


FIG. 5D

FIG. 5E



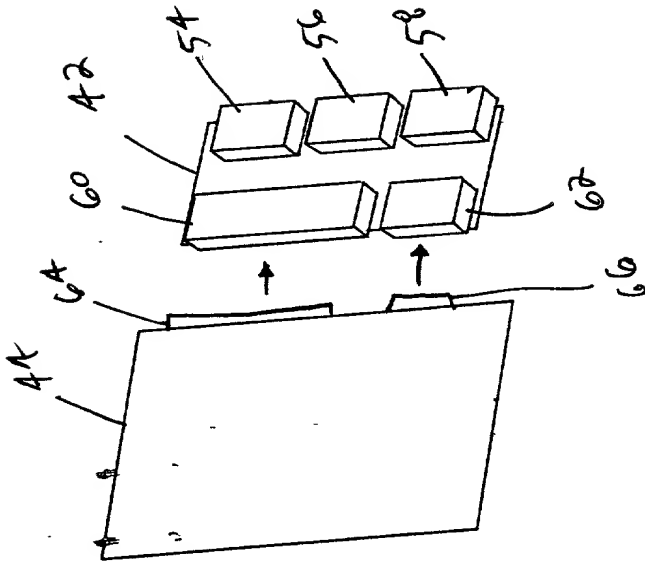
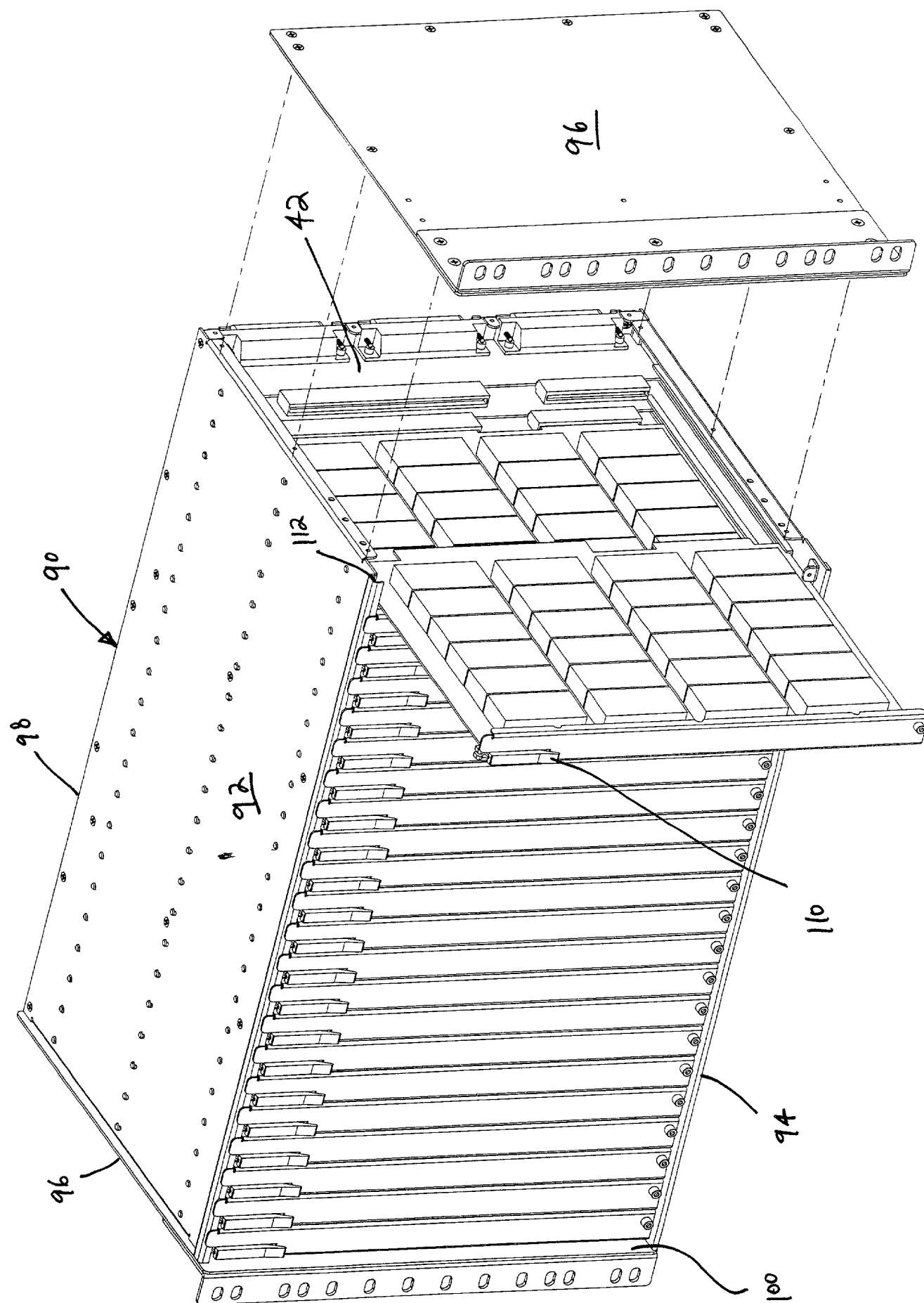


FIG. 6

FIG. 16.7

FIG. 16.7



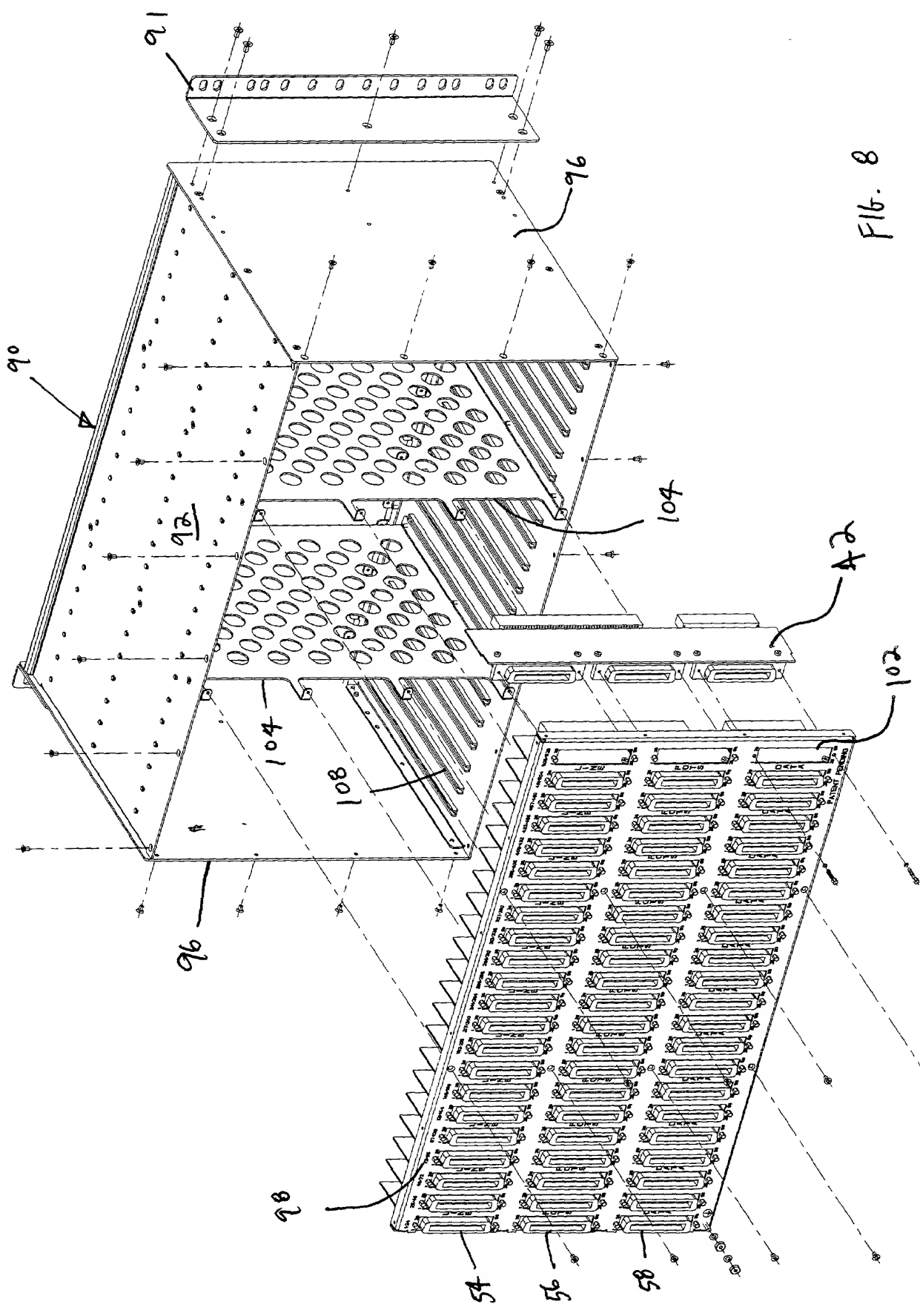


FIG. 8

FIG. 9 is a plan view of a circuit board 91 showing a grid of 10 columns and 3 rows of components. The columns are labeled LINE, POTS, and DATA. The rows are labeled 1, 2, and 3. The components are labeled with their part numbers and values. The board is shown with a top layer of components and a bottom layer of components. The board is shown with a top layer of components and a bottom layer of components.

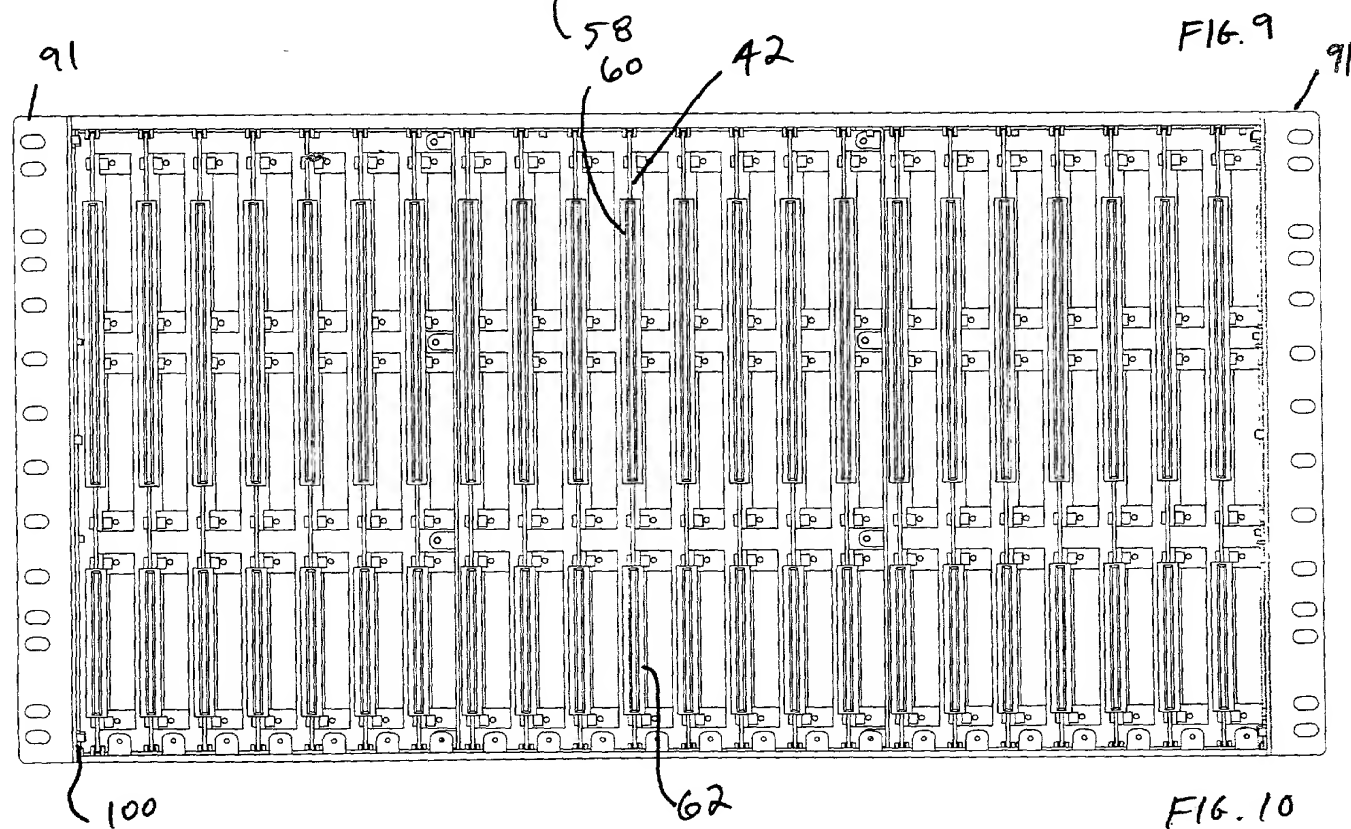
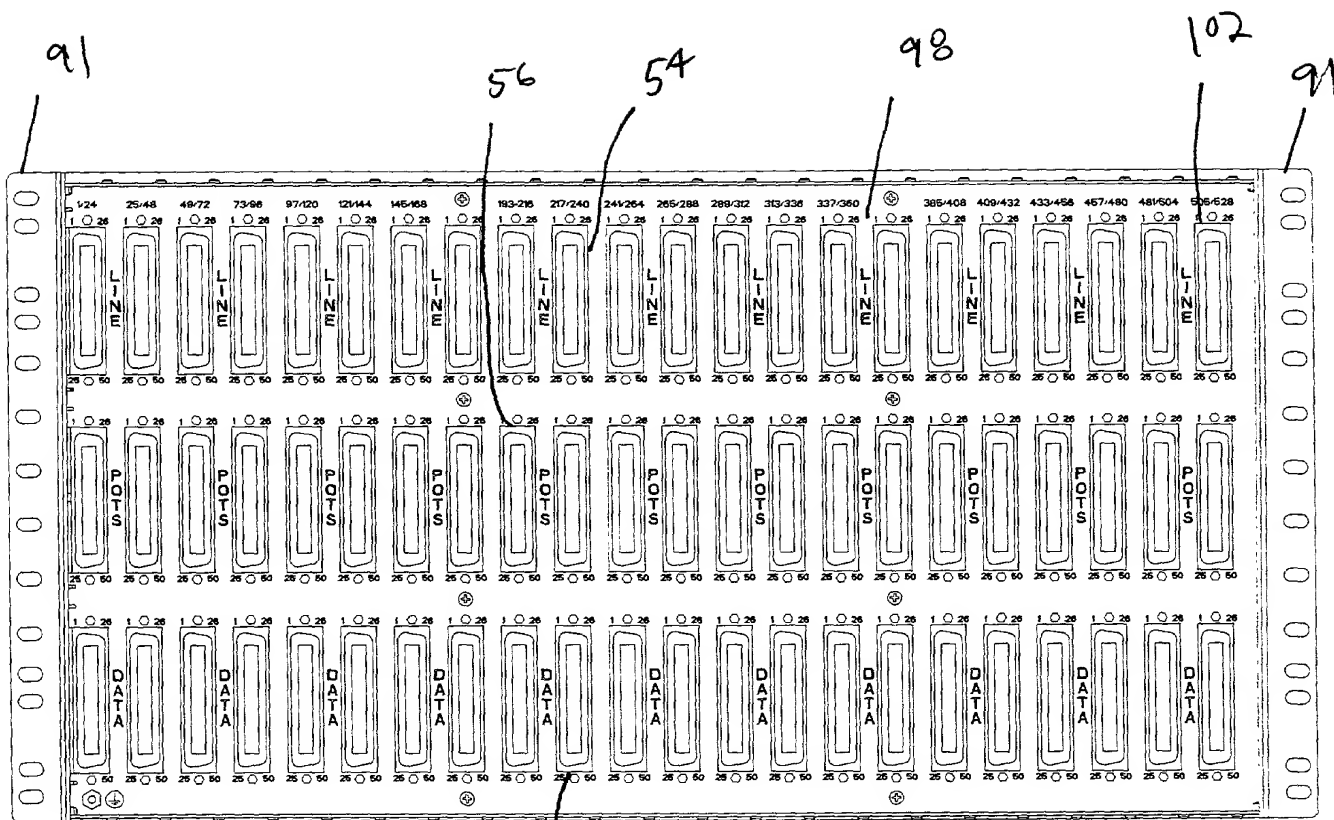


FIG. 11 is a perspective view of a multi-layered structure 100, showing a top layer 102, a middle layer 104, and a bottom layer 106. The top layer 102 is a rectangular plate with a grid of small circles. The middle layer 104 is a rectangular plate with a grid of larger circles. The bottom layer 106 is a rectangular plate with a grid of small circles. The layers are stacked on top of each other, with the top layer 102 on top, the middle layer 104 in the middle, and the bottom layer 106 at the bottom. The structure 100 is shown in a perspective view, with dashed lines indicating the edges and the layers.

FIG. 11

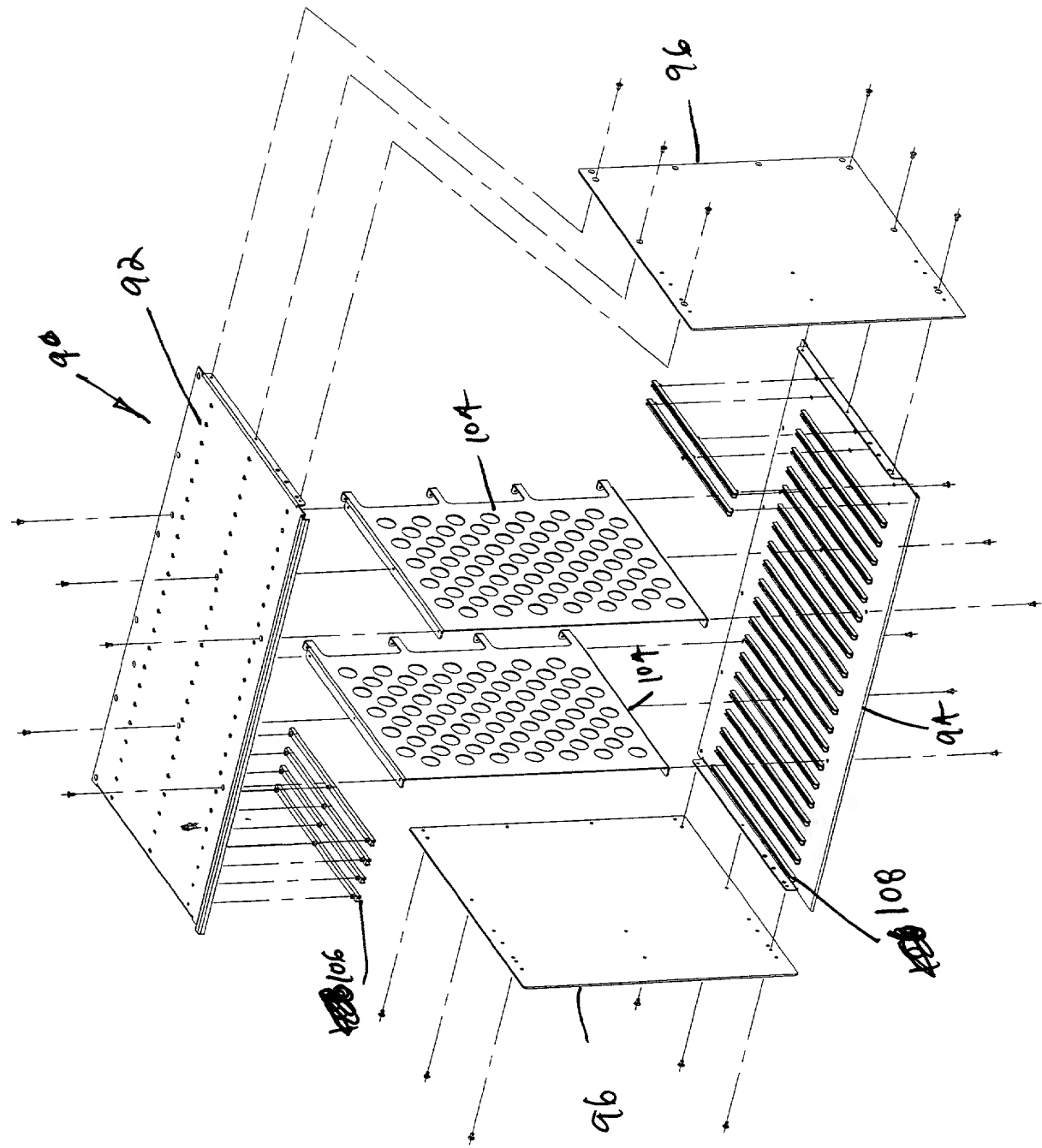
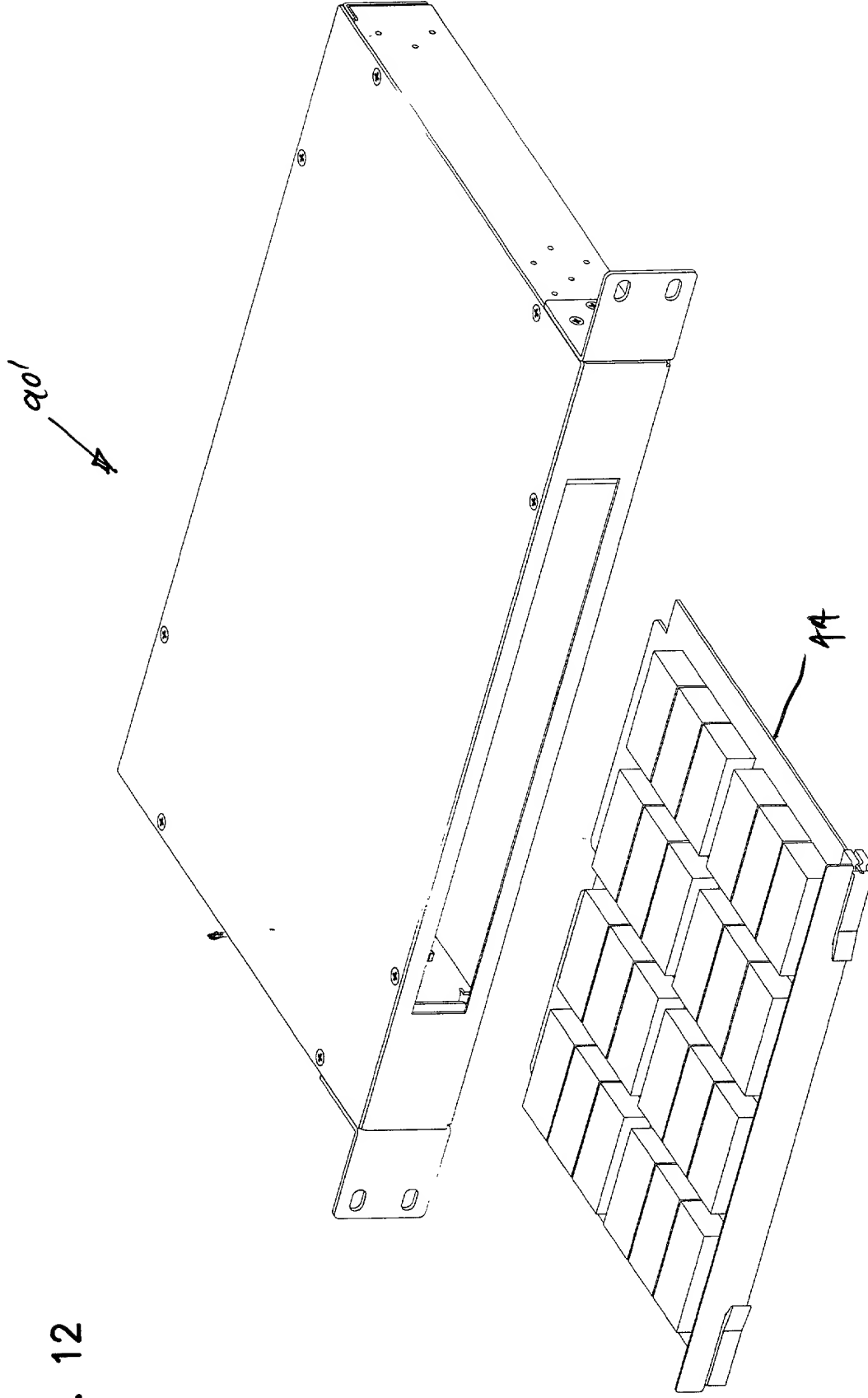


FIG. 12



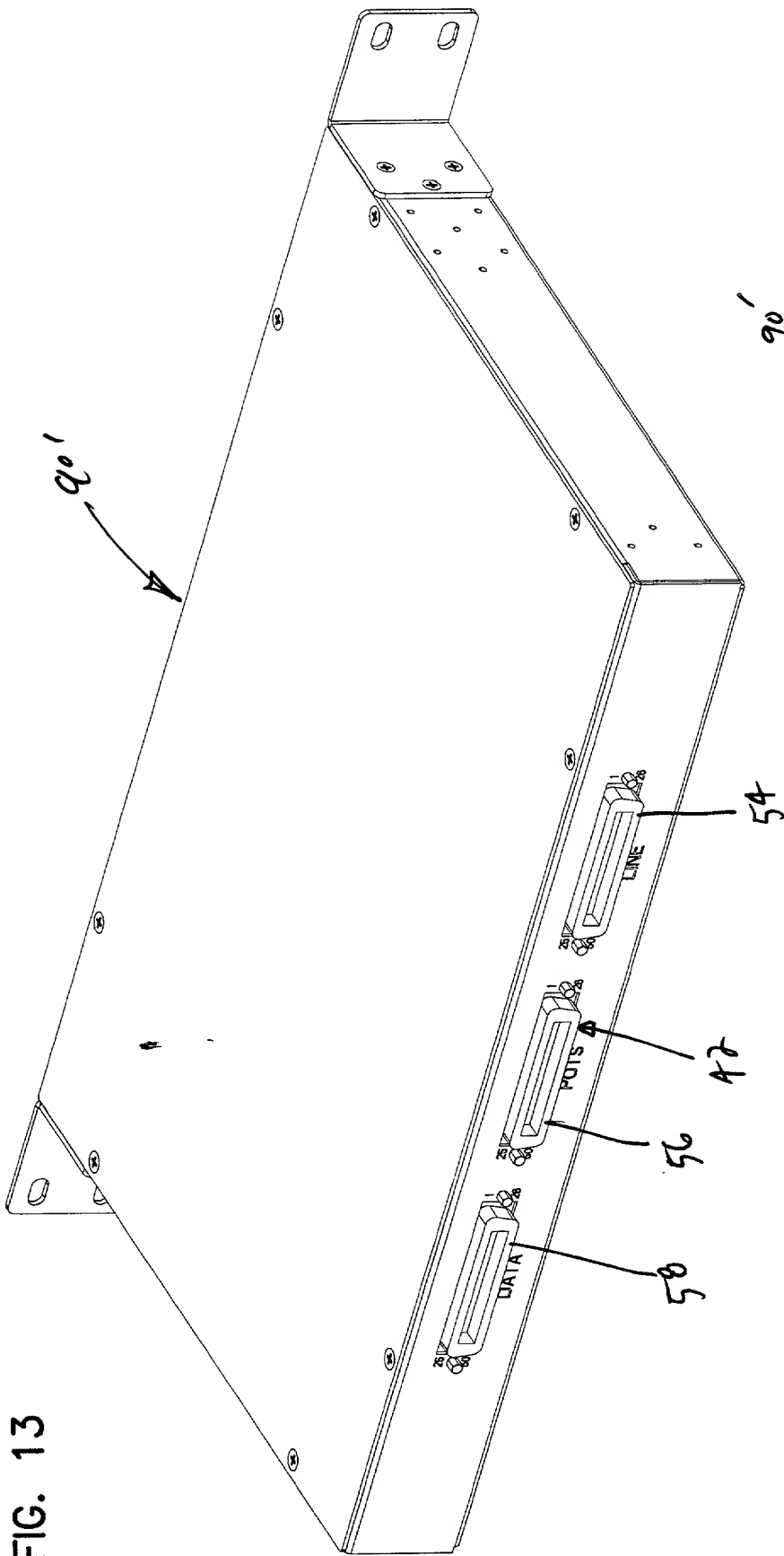


FIG. 13

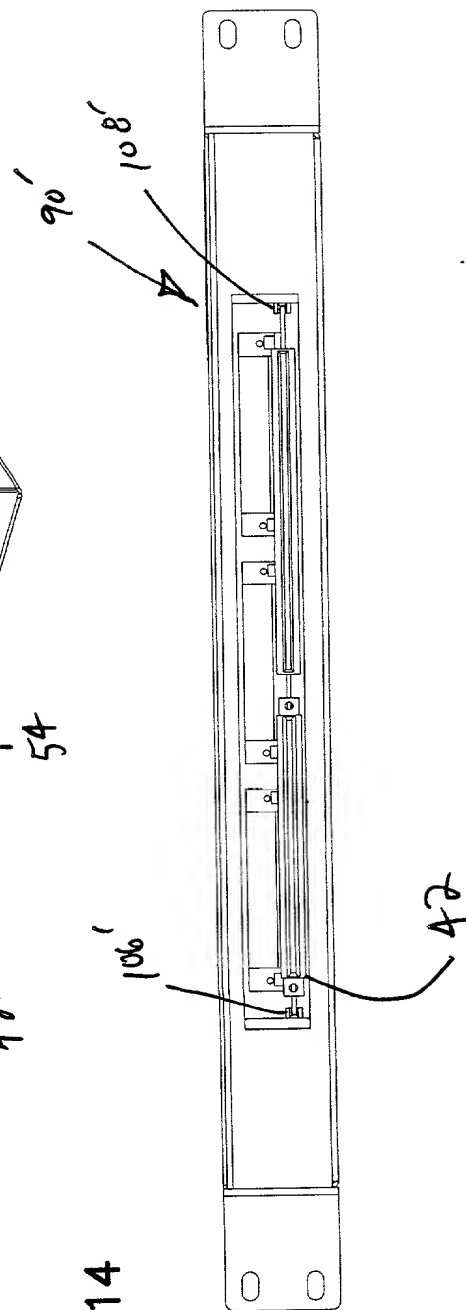


FIG. 14

MERCHANT & GOULD P.C.

United States Patent Application

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that

I verily believe I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **SPLITTER ARCHITECTURE FOR A TELECOMMUNICATIONS SYSTEM.**

The specification of which

a. ☐ is attached hereto

b. ☒ is entitled **SPLITTER ARCHITECTURE FOR A TELECOMMUNICATIONS SYSTEM**, having an attorney docket number **2316.1175US01**.

*c. ☒ was filed on as application serial no. and was amended on (if applicable) (in the case of a PCT-filed application) described and claimed in international no. filed and as amended on (if any), which I have reviewed and for which I solicit a United States patent.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, § 1.56 (attached hereto).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on the basis of which priority is claimed:

a. ☒ no such applications have been filed.

b. ☐ such applications have been filed as follows:

FOREIGN APPLICATION(S), IF ANY, CLAIMING PRIORITY UNDER 35 USC § 119			
COUNTRY	APPLICATION NUMBER	DATE OF FILING (day, month, year)	DATE OF ISSUE (day, month, year)
ALL FOREIGN APPLICATION(S), IF ANY, FILED BEFORE THE PRIORITY APPLICATION(S)			
COUNTRY	APPLICATION NUMBER	DATE OF FILING (day, month, year)	DATE OF ISSUE (day, month, year)

I hereby claim the benefit under Title 35, United States Code, § 120/365 of any United States and PCT international application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. APPLICATION NUMBER	DATE OF FILING (day, month, year)	STATUS (patented, pending, abandoned)

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

U.S. PROVISIONAL APPLICATION NUMBER	DATE OF FILING (Day, Month, Year)

I hereby appoint the following attorney(s) and/or patent agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith:

Albrecht, John W.	Reg. No. 40,481	Lacy, Paul E.	Reg. No. 38,946
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Black, Bruce E.	Reg. No. 41,622	McDonald, Daniel W.	Reg. No. 32,044
Branch, John W.	Reg. No. 41,633	McIntyre, Jr., William F.	Reg. No. P-44,921
Bremer, Dennis C.	Reg. No. 40,528	Mueller, Douglas P.	Reg. No. 30,300
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Gould, John D.	Reg. No. 18,223	Swenson, Erik G.	Reg. No. 45,147
Gregson, Richard	Reg. No. 41,804	Tellekson, David K.	Reg. No. 32,314
Gresens, John J.	Reg. No. 33,112	Trembath, Jon R.	Reg. No. 38,344
Hamre, Curtis B.	Reg. No. 29,165	Underhill, Albert L.	Reg. No. 27,403
Hillson, Randall A.	Reg. No. 31,838	Vandenburgh, J. Derek	Reg. No. 32,179
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Karjeker, Shaukat	Reg. No. 34,049	Whipps, Brian	Reg. No. 43,261
Kastelic, Joseph M.	Reg. No. 37,160	Wickhem, J. Scot	Reg. No. 41,376
Kettelberger, Denise	Reg. No. 33,924	Williams, Douglas J.	Reg. No. 27,054
Keys, Jeramie J.	Reg. No. 42,724	Witt, Jonelle	Reg. No. 41,980
Knearl, Homer L.	Reg. No. 21,197	Wu, Tong	Reg. No. 43,361
Kowalchyk, Alan W.	Reg. No. 31,535	Xu, Min S.	Reg. No. 39,536
Kowalchyk, Katherine M.	Reg. No. 36,848	Zeuli, Anthony R.	Reg. No. 45,255

I hereby authorize them to act and rely on instructions from and communicate directly with the person/assignee/attorney/firm/ organization who/which first sends/sent this case to them and by whom/which I hereby declare that I have consented after full disclosure to be represented unless/until I instruct Merchant & Gould P.C. to the contrary.

Please direct all correspondence in this case to Merchant & Gould P.C. at the address indicated below:

Merchant & Gould P.C.
P.O. Box 2903
Minneapolis, MN 55402-0903

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

201	Full Name Of Inventor	Family Name SWAM	First Given Name STEVEN	Second Given Name M.
	Residence & Citizenship	City	State or Foreign Country	Country of Citizenship
	Post Office Address	Post Office Address	City	State & Zip Code/Country
Signature of Inventor 201:			Date:	
202	Full Name Of Inventor	Family Name SIT	First Given Name ERIC	Second Given Name
	Residence & Citizenship	City	State or Foreign Country	Country of Citizenship
	Post Office Address	Post Office Address	City	State & Zip Code/Country
Signature of Inventor 202:			Date:	
203	Full Name Of Inventor	Family Name MORGENSTERN	First Given Name TODD	Second Given Name
	Residence & Citizenship	City	State or Foreign Country	Country of Citizenship
	Post Office Address	Post Office Address	City	State & Zip Code/Country
Signature of Inventor 203:			Date:	

§ 1.56 Duty to disclose information material to patentability.

(a) A patent by its very nature is affected with a public interest. The public interest is best served, and the most effective patent examination occurs when, at the time an application is being examined, the Office is aware of and evaluates the teachings of all information material to patentability. Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability as defined in this section. The duty to disclose information exists with respect to each pending claim until the claim is canceled or withdrawn from consideration, or the application becomes abandoned. Information material to the patentability of a claim that is canceled or withdrawn from consideration need not be submitted if the information is not material to the patentability of any claim remaining under consideration in the application. There is no duty to submit information which is not material to the patentability of any existing claim. The duty to disclose all information known to be material to patentability is deemed to be satisfied if all information known to be material to patentability of any claim issued in a patent was cited by the Office or submitted to the Office in the manner prescribed by §§ 1.97(b)–(d) and 1.98. However, no patent will be granted on an application in connection with which fraud on the Office was practiced or attempted or the duty of disclosure was violated through bad faith or intentional misconduct. The Office encourages applicants to carefully examine:

- (1) prior art cited in search reports of a foreign patent office in a counterpart application, and
 - (2) the closest information over which individuals associated with the filing or prosecution of a patent application believe any pending claim patentably defines, to make sure that any material information contained therein is disclosed to the Office.
- (b) Under this section, information is material to patentability when it is not cumulative to information already of record or being made of record in the application, and
- (1) It establishes, by itself or in combination with other information, a prima facie case of unpatentability of a claim;
 - (2) It refutes, or is inconsistent with, a position the applicant takes in:
 - (i) Opposing an argument of unpatentability relied on by the Office, or
 - (ii) Asserting an argument of patentability.

A prima facie case of unpatentability is established when the information compels a conclusion that a claim is unpatentable under the preponderance of evidence, burden-of-proof standard, giving each term in the claim its broadest reasonable construction consistent with the specification, and before any consideration is given to evidence which may be submitted in an attempt to establish a contrary conclusion of patentability.

- (c) Individuals associated with the filing or prosecution of a patent application within the meaning of this section are:
- (1) Each inventor named in the application;
 - (2) Each attorney or agent who prepares or prosecutes the application; and
 - (3) Every other person who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application.
- (d) Individuals other than the attorney, agent or inventor may comply with this section by disclosing information to the attorney, agent, or inventor.